	Application No.	Applicant(s)
Notice of Allowability	10/749,855	KWA ET AL.
	Examiner	Art Unit
	Nitin C. Patel	2116
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>amendment filed on 1 August 2006</u> .		
2. The allowed claim(s) is/are 1-2,4-12,14-18,20-24, and26-28.		
 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some* c) None of the: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). 		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached		
1) 🗌 hereto or 2) 🗍 to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
ldentifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s)	E Notice of Informal C	Patent Application (PTO-152)
 Notice of References Cited (PTO-892) Notice of Draftperson's Patent Drawing Review (PTO-948) 	6. Interview Summary	
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mail Dat	te
Paper No./Mail Date	•	
 Examiner's Comment Regarding Requirement for Deposit of Biological Material 	8. ⊠ Examiner's Stateme 9. □ Other	ent of Reasons for Allowance
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DETAILED ACTION

1. This is in responsive to amendment filed on 1 August 2006.

- 2. Claims 3,13,19, and 25 have been cancelled.
- 3. Claims 1-2, 4-12, 19-24, and 26-28 are allowed.

Reasons For Allowance

With respect to claims 1,12, 18, and 24, the prior art fails to disclose or suggest 4. "de-asserting an indicator within a message packet to allow the processor to enter into the power state in response to a request from the processor to enter into the power state if a plurality of incoherent transactions pending in the buffer of the chipset device exceeds a second threshold", "the power management circuitry de-asserts an indicator within a second message packet to allow the processor to enter into the power state if a plurality of incoherent transactions pending in the buffer of the root complex device exceeds a second threshold", "the memory controller to allow a processor in the computing system to enter into the power state in response to a request from the processor to enter into a power state if a plurality of incoherent transactions pending in a buffer of the memory controller exceeds an entry threshold, and to prevent the processor from entering into the power state if the plurality of incoherent transactions is below the entry threshold and to cause the processor to exit from the power state if a plurality of coherent transactions pending in the buffer of the memory controller exceeds an exit threshold", and "the power management circuitry de-asserts an indicator within the second message packet in response to a request from the processor to enter into the power state to allow the processor to enter into the power state if a plurality of

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incoherent transactions pending in the buffer of the memory controller exceeds a second threshold "respectively. Claims 2, 4 - 11, 14 - 17, 20 - 23, and 26 - 28 depend from claims 1, 12, 18, and 124 respectively, and likewise are allowable.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin C. Patel whose telephone number is 571-272-3675. The examiner can normally be reached on 6:30 am - 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne H. Browne can be reached on 571-272-3670. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Nitin C. Patel August 9, 2006 LYNNE H. BROWNE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100